**論文著述**

**(一)期刊論文**

1. **Chin-Hsiung Wu, S. J. Horng and H. R. Tsai (Sept. 2000), “Efficient Parallel Algorithms for Hierarchical Clustering on Arrays with Reconfigurable Optical Buses,” Journal of Parallel and Distributed Computing, vol. 60, pp.1137--1153. (SCI, EI paper) (NSC88-2213-E-011-082)**

2. **Chin-Hsiung Wu, S. J. Horng Y. W. Chen and W. Y. Lee (Oct. 2000), “Designing Scalable and Efficient Parallel Clustering Algorithms on Arrays with Reconfigurable Optical Buses,” Image and Vision Computing, vol. 18, pp. 1033--1043. (SCI, EI paper) (NSC88-2213-E-011-082)**

3. **Chin-Hsiung Wu, S. J. Horng and Y. R. Wang (2000), “Computing Shape’s Moments via Rectangular Decomposition on Arrays** with **Reconfigurable Optical Buses,” International Journal of Applied Mathematics, vol. 3, no. 1, pp. 91--106. (NSC88-2213-E-011-082)**

4. **Chin-Hsiung Wu, S. J. Horng and P. Z. Lee (2001), “A New Computation of Shape Moments via Quadtree Decomposition,” Pattern Recognition, vol. 34, no. 7, pp. 1319--1330. (SCI paper) (NSC89-2213-E-011-007)**

5. **Chin-Hsiung Wu and S. J. Horng (2001), "Vector Median Filters on Arrays with Reconfigurable Optical Buses," IEEE Transactions on Parallel and Distributed Systems, vol. 12, no. 12, pp. 1281--1292. (SCI paper) (NSC89-2213-E-011-007)**

6. **Chin-Hsiung Wu, S. J. Horng and H. R. Tsai (2002), "Optimal Parallel Algorithms for Computer Vision Problems," Journal of Parallel and Distributed Computing, vol. 62, no. 6, 1021--1041. (SCI, EI paper) (NSC90-2623-7-012-002)**

7. **Chin-Hsiung Wu and S. J. Horng (2003), “Scalable and Optimal Speed-up Parallel Algorithms for Template Matching on Arrays with Reconfigurable Optical buses,” Int. Journal of Foundations of computer science, vol. 14, no. 1, pp.79--98. (NSC90-2623-7-012-002)**

8. **Chin-Hsiung Wu and S. J. Horng (2003), "Fast and Scalable Selection Algorithms with Applications to Median Filtering," IEEE Transactions on Parallel and Distributed Systems, vol. 14, no. 10, pp. 983--992. (SCI, EI paper) (NSC90-2623-7-012-002).**

9. **Chin-Hsiung Wu and S. J. Horng (2003), "Run-length Chain Coding and Scalable Computation of a Shape's Moments Using Reconfigurable Optical Buses," IEEE Transactions on Systems, Man and Cybernetics, Part B: Cybernetics, vol. 34, no. 2, pp. 845--855. (SCI, EI paper) (NSC91-2623-7-012-001).**

10. **Y. R. Wang, S. J. Horng and Chin-Hsiung Wu (2005), "Efficient Algorithms for the All Nearest Neighbor and Closest Pair Problems on the Linear Array with a Reconfigurable Pipelined Bus System," IEEE Transactions on Parallel and Distributed Systems, vol. 16, no. 3, pp. 193--206. (SCI, EI paper) (NSC 92-2213-E-012-001).**

11. **Chin-Hsiung Wu, S. J. Horng, Y. R. Wang and H. R. Tsai (2006), "Optimal geometric algorithms for digitized images on arrays with reconfigurable optical buses," Microprocessors and Microsystems, vol. 30, no. 7, pp. 425-434. (SCI, EI paper) (NSC 92-2213-E-012-001).**

12. **Chin-Hsiung Wu, S.J. Horng, C. F. Wen, Y.R. Wang (2008), "Fast and Scalable Computations of 2-D Image Moments," Image and Vision Computing, vol. 26, no. 6, pp.799-811. (SCI, EI paper) (NSC93-2213-E-158-002)**

13. **吳金雄，資訊科技與海軍官校教學，＜海軍軍官＞，vol. 21, no. 4, pp. 4--11, 2002.**

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**15. 吳金雄，** **私立大學招生策略之探討 —以S大學為例, 明道學術論壇9(1)：3-18(2014).**

**(二)研討會論文**

**1. Chin-Hsiung Wu, R. C. Tsai, S. J. Horng and H. R. Tsai, "Hierarchical Clustering on Arrays with Reconfigurable Optical Buses," in Proceedings of International Computer Symposium on Algorithms (ICS'98), National Cheng Kung University,** Tainan**, Taiwan, pp. 14--21, Dec. 17--19, 1998.**

**2. Chin-Hsiung Wu, S. J. Horng, R. C. Tsai and H. T. Chang, "Parallel Clustering Algorithms Based on The Minimum Spanning Tree," in Proceedings of The 1st Japanese-Hungarian Symposium on Discrete Mathematics and Its Applications,** Kyoto**, Japan, pp. 133--140, March 17--19, 1999.**

**3. Chin-Hsiung Wu, S. J. Horng, H. R. Tsai, S. S. Lee and Y. W. Chen, "Optimal Digital Geometry Algorithms on Arrays with** Reconfigurable **Optical Buses," in Proceedings of IASTED International Conference on Parallel and Distributed Computing and Systems (PDCS'99), MIT, MA., USA, pp. 479--485, Nov. 3--6, 1999.**

**4. Chin-Hsiung Wu, S. J. Horng, H. R. Tsai, S. S. Lee and S. Y. Lin, "Scalable and Efficient Clustering Algorithms on Arrays with Reconfigurable Optical Buses," in Proceedings of IASTED International Conference on Parallel and Distributed** Computing **and Systems (PDCS'99), MIT, MA., USA, pp. 44--49, Nov. 3--6, 1999.**

**5. Chin-Hsiung Wu, S. J. Horng, P. Z. Lee, S. S. Lee and S. Y. Lin, "A New Computation of Shape Moments via Quadtree** Decomposition**," in Proceedings of the IPDPS 2000 Workshop on Advances in Parallel and Distributed Computational Models (Lecture Notes in Computer Science LNCS 1800), Cancun, Mexico, pp. 123--129, May 1--5, 2000.**

**6. Chin-Hsiung Wu, S. J. Horng, H. R. Tsai, J. F. Lin and T. L. Lin, "An Optimal Parallel Algorithm for Computing Moments on Arrays with Reconfigurable Optical Buses," in Proceedings of IEEE International Parallel and Distributed Processing Symposium (IPDPS2000), Cancun, Mexico, pp. 741--746, May 1--5, 2000 (EI).**

**7. Chin-Hsiung Wu, S. J. Horng and P. Z. Lee, "Image Rectangular Decomposition on a Reconfigurable Mesh," in Proceedings of the International Computer Symposium on Algorithms (ICS'2000), National Chung Cheng University, Chiayi, T**aiwan**, pp. 62--69, Dec. 6--8, 2000.**

**8. Chin-Hsiung Wu, S. J. Horng and Y. R. Wang, "Parallel Algorithm for Computing Shape's Moments on Arrays with Reconfigurable** Optical **Buses," in Proceedings of the 4th International Conference on Algorithms and Architectures for Parallel Processing (ICA3PP2000), Hong Kong, pp. 136--147, Dec. 11--13, 2000.**

**9. Chin-Hsiung Wu, S. J. Horng, Y. R. Wang and L. G. Jeng, "Parallel Algorithms for Vector Median Filtering," in Proceedings of the** 4th **International Conference on Algorithms and Architectures for Parallel Processing (ICA3PP2000), Hong Kong, pp. 240--251, Dec. 11--13, 2000.**

**10. Chin-Hsiung Wu, "Histogramming and histogram modification on arrays with reconfigurable optical buses," in Proceedings of the 2nd International Conference on Parallel and Distributed Computing, Applications and Technologies, Taipei, pp. 109--116, July. 9--11, 2001.**

**11. Chin-Hsiung Wu, S. J. Horng, Y. W. Chen and C. K. Yu, "Run-length Chain Coding and Shape’s Moment Computations on Arrays with Reconfigurable Optical Buses," in Proceedings of the IEEE International Conference on Parallel Processing (ICPP2001), Spansh, pp. 479--486, Sept. 3--7, 2001.**

**12. 吳金雄，鄭立國，**洪西進**，田輝莒，張蓮生，林淼，”軍事院校實施多元學制與課程之研究”，軍事教育研討會，台北, 12月2001.**

**13. Chin-Hsiung Wu, S. J. Horng and Yi Pan, "Parallel Algorithms for Median Filtering on Arrays with Reconfigurable Optical Buses," in Proceedings of the IEEE International Parallel and Distributed Processing Symposium (IPDPS2002), Fort Lauderdale, Florida, USA, April 15--19, 2002.**

**14. Y. R. Wang, S. J. Horng and Chin-Hsiung Wu, "An O(1) Time Parallel Algorithm for the 3D Euclidean Distance Transform on Arrays** with **Reconfigurable Optical Buses", in Proceedings of The 2002 International Conference on Parallel and Distributed Processing Techniques and Applications, Nevada, USA, June 24 --27, 2002.**

**15. T. Y. Wang, S. J. Horng, M. Y. Su, Chin-Hsiung Wu, P. C. Wang and W. Z. Su, "A Surveillance Spyware Detection System Based on Data Mining Methods," in Proceedings of the 2006 IEEE Congress on Evolutionary Computation, Vancouver, Canada, pp. 11005-11010, July 16-21, 2006 (EI).**

16. **吳金雄，王子彥，”執行檔型病毒偵測之研究”， 資訊安全會議，嘉義, 6月2007.**

17.**T.-Y. Wang and Chin-Hsiung Wu, "A Virus Prevention Model Based on Static Analysis and Data Mining Methods", in Proceedings of the IEEE 8th International Conference on Computer and Information Technology, Sydney, Australia, July 8-11, 2008 (EI).**

**18. T. Y. Wang, Chin-Hsiung Wu and C. C. Hsieh, “Detecting Unknown Malicious Executables Using Portable Executable Headers,” in Proc. Fifth Int’l Joint Conference on INC, IMS and IDC, pp. 278—284, 2009. (EI).**

20. **T. Y. Wang & Chin-Hsiung Wu,** “Detection of packed executables using support vector machines,” Proc. of Int'l Conf. on Machine Learning and Cybernetics 2011.

21. **Chin-Hsiung Wu**, S.-J. Horng, and S. T. Chung, “An Efficient Microblog Spam Detector Using User-Generated Context and Behavior,” Proc. of Int'l Conf. on Computing and Security(ICCS'12).

22. **Chin-Hsiung Wu,** C.-T. Lin and S.-J. Horng, ” Implementation of Module Replacement Mechanism for Partial Reconfigurable Architecture,” Proc. Of Int'l Conf. on Engineering and Applied Science, 2014.